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Date: December 16, 2005

To: United States Patent and Trademark Office
Examiner: Sarkar, Asok K.; Art Unit: 2891

Fax: 571-273-8300

Re: **Application Serial No.: 10/762,445**
Filing Date: 1/22/2004; First Named Inventor: Fang
Attorney Docket No.: 0180192

From: Farjami & Farjami LLP

Number of pages including the cover sheet: 9

Message:

Enclosed please find a Response to the Final Office Action dated October 25, 2005.

Thank you.

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Attorney Docket No.: 0180192

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: **Fang, et al.**

Art Unit: 2891

Serial No.: 10/762,445

Examiner: SARKAR, ASOK K.

Filed: 01/22/2004

For: **Structure and Method for Low VSS
Resistance and Reduced DIBL in a
Floating Gate Memory Cell**

RESPONSE TO FINAL OFFICE ACTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir/Madam:

This is in response to the Final Office Action dated October 25, 2005 in the above-referenced patent application. Please enter and consider the following remarks.